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Substitute for form 1449A/PTO

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Sheet 1 of 5

Complete if Known

Application Number	10/757,866
Filing Date	January 16, 2004
First Named Inventor	Craig C. HANSEN, et al.
Group Art Unit	2183
Examiner Name	COLEMAN, ERIC
Attorney Docket Number	043876-0156

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code* (if known)	MM-DD-YYYY		
		US-5,327,369	07-05-1994	Ashkenazi	
		US-5,390,135	02-14-1995	Lee	
		US-5,448,509	09-05-1995	Lee	
		US-5,467,131	11-14-1995	Bhaskaran	
		US-5,541,865	07-30-1996	Ashkenazi	
		US-5,590,350	12-31-1996	Guttag	
		US-5,883,824	03-16-1999	Lee	
		US-5,933,160	08-03-1999	Kabir et al.	
		US-6,058,465	05-02-2000	Nguyen	

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	/Eric Coleman/	Date Considered	07/14/2008
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Sheet	2	of	5	Attorney Docket Number	043876-0156

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T ²
		MU v. DELL & INTEL 2/25/05 Defendant Dell Inc.'s Amended Answer, Affirmative Defenses, and Counterclaims to Plaintiff's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		2/25/05 Defendant Intel Corporation's Amended Answer, Affirmative Defenses, and Counterclaims to Plaintiff's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		3/9/05 Joint Claim Construction and Prehearing Statement with Exhibits A through F-2 (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		4/11/05 MicroUnity Systems Engineering, Inc.'s Opening Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(a) and Exhibits A-1 (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		4/26/05 Supplement to Plaintiff MicroUnity Systems Engineering, Inc.'s Opening Brief Regarding Claim Construction (submitting a corrected page 41) (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		5/12/05 Dell, Inc. and Intel Corporation's Responsive Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(b) (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		5/25/05 MicroUnity's Reply Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(c) (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		6/9/05 Intel and Dell's Surreply Brief Regarding Claim Construction (<i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		MU v. SCEA 2/14/06 SCEA's Answer, Affirmative Defenses, and Counterclaim to MicroUnity's Original Complaint (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
		5/1/07 Exhibits A-2, E-2 and F-2 to SCEA's Supplemental Invalidity Contentions, <i>previously marked Outside Counsel Eyes Only</i> , but <i>now released</i> (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
		7/11/07 SCEA's Second Amended Answer, Affirmative Defenses, and Counterclaims to MicroUnity's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		7/11/07 P.R. 4-3 Joint Claim Construction Statement (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	

Examiner Signature	/Eric Coleman/	Dated Considered	07/14/2008
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			Examiner Name	COLEMAN, ERIC
			Attorney Docket Number	043876-0156
Sheet	3	of	5	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T ²
	7/25/07 MU's Answer to SCEA's Second Amended Counterclaim in Response to MU's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	8/24/07 MU's LRP 4-5(a) Opening Brief on Claim Construction, and Exhibits 1-14 (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	9/12/07 SCEA's Responsive Brief Regarding Claim Construction Pursuant to P.R. 4-5(b), and Exhibits 1-34 (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	9/13/07 SCEA's Unopposed Motion to Supplement P.R. 4-3 Joint Claim Construction Statement, and Proposed Order (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	9/13/07 Notice of Filing of P.R. 4-5(d) Joint Claim Construction Chart, and Exhibit 1 (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Div)		
	9/14/07 Order Granting SCEA's Unopposed Motion to Supplement P.R. 4-3 Joint Claim Construction Statement (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	9/17/07 LPR 4-5(c) Reply Brief on Claim Construction from MicroUnity Systems Engineering, Inc., and Exhibits 15 and 16 (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	9/20/07 Transcript of Claim Construction Hearing Before the Honorable T. John Ward United States District Judge (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	10/15/07 SCEA's Motion for Partial Summary Judgment of Invalidity for U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order, and Exhibits A-U (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	10/29/07 MU's Response Brief in Opposition to Sony's Motion for Partial Summary Judgment of Invalidity of U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	10/29/07 Declaration of Michael Heim in Support of MU's Response Brief in Opposition to Sony's Motion for Partial Summary Judgment of Invalidity of U.S. Patent Nos. 6,643,765 and 6,725,356, and Exhibits A-P (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	11/6/07 Order Granting Joint Motion to Stay Litigation Pending Settlement (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		
	12/12/07 Order of Dismissal with Prejudice and Final Judgment (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)		

Examiner Signature	/Eric Coleman/	Dated Considered	07/14/2008
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

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		MU v. AMD 8/6/07 AMD's Invalidity Contentions Under Patent Rule 3-3, and Exhibits A-I (<i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		9/5/07 AMD's Supplemental and Consolidated Invalidity Contentions -- LPR 3-3, and Exhibits A-L (<i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		9/13/07 First Amended Complaint, and Exhibits A-K (<i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		10/1/07 AMD's Answer to First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		1/8/08 Order of Dismissal With Prejudice and Final Judgment (<i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		The AMD-K6 3D Processor: Revolutionary Multimedia Performance , Ed. H. Kalish and J. Isaac, Abacus (1998)	
		BIT Data Sheet - Product Summary: B3110/B3120/B2110/B2120 Floating Point Chip Set, Bipolar Integrated Technology, Inc. (Dec. 1986)	
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		Colwell et al., "A VLIW Architecture for a Trace Scheduling Compiler," <i>IEEE Transactions on Computers</i> , vol.37, no.8, 967-79 (Aug. 1988)	
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		Hansen, "MicroUnity's MediaProcessor Architecture," <i>IEEE Micro</i> (Aug. 1996)	
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		Kuck, "The Structure of Computers and Computation: Volume 1," John Wiley & Sons, Inc. (1978)	
		Lowney et al., "The Multiflow Trace Scheduling Compiler," (Oct. 30, 1992)	
		"MIPS Digital Media Extension" (MDMX), rev. 1.0, C-1 to C-40	
		"Multimedia Extension Unit for the X86 Architecture," Compaq Computer Corp., Revision 0.8b (Jun. 20, 1995)	
		"PA-RISC 2.0 Architecture and Instruction Set Reference Manual," Hewlett-Packard Co. (1995)	
		Rathnam et al., "An Architectural Overview of the Programmable Multimedia Processor, TM-1," <i>Proceedings of COMPCON '96</i> , IEEE (Spring 1996)	
		Rubinfeld, et al., "Motion Video Instruction Extensions for Alpha," Semiconductor Eng'g Group (Oct. 18, 1996)	

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